## PAPER-II <br> ELECTRONIC SCIENCE

## Signature and Name of Invigilator

1. (Signature)
(Name)
2. (Signature)
(Name)

\section*{| 8816 |
| :--- | :--- | :--- |}

OMR Sheet No. :
(To be filled by the Candidate)
Roll No.

(In figures as per admission card)
Roll No.
(In words)
$\qquad$

Roll No.

Time : $1 \frac{1}{4}$ hours]

## Number of Pages in this Booklet : 16

## Instructions for the Candidates

1. Write your roll number in the space provided on the top of this page.
2. This paper consists of fifty multiple-choice type of questions.
3. At the commencement of examination, the question booklet will be given to you. In the first 5 minutes, you are requested to open the booklet and compulsorily examine it as below :
(i) To have access to the Question Booklet, tear off the paper seal on the edge of this cover page. Do not accept a booklet without sticker-seal and do not accept an open booklet.
(ii) Tally the number of pages and number of questions in the booklet with the information printed on the cover page. Faulty booklets due to pages/questions missing or duplicate or not in serial order or any other discrepancy should be got replaced immediately by a correct booklet from the invigilator within the period of 5 minutes. Afterwards, neither the Question Booklet will be replaced nor any extra time will be given.
(iii) After this verification is over, the Test Booklet Number should be entered on the OMR Sheet and the OMR Sheet Number should be entered on this Test Booklet.
4. Each item has four alternative responses marked (1), (2), (3) and (4). You have to darken the circle as indicated below on the correct response against each item.

## Example : (1) (2) (4)

where (3) is the correct response.
5. Your responses to the items are to be indicated in the OMR Sheet given inside the Booklet only. If you mark your response at any place other than in the circle in the OMR Sheet, it will not be evaluated.
6. Read instructions given inside carefully.
7. Rough Work is to be done in the end of this booklet.
8. If you write your Name, Roll Number, Phone Number or put any mark on any part of the OMR Sheet, except for the space allotted for the relevant entries, which may disclose your identity, or use abusive language or employ any other unfair means, such as change of response by scratching or using white fluid, you will render yourself liable to disqualification.
9. You have to return the Original OMR Sheet to the invigilators at the end of the examination compulsorily and must not carry it with you outside the Examination Hall. You are, however, allowed to carry original question booklet and duplicate copy of OMR Sheet on conclusion of examination.
10. Use only Black Ball point pen provided by C.B.S.E.
11. Use of any calculator or log table etc., is prohibited.
12. There is no negative marks for incorrect answers.

## ELECTRONIC SCIENCE <br> Paper - II

Note : This paper contains fifty (50) objective type questions of two (2) marks each. All questions are compulsory.

1. In a circuit shown below the base current is

(1) $2.55 \mu \mathrm{~A}$
(2) $5.4 \mu \mathrm{~A}$
(3) $2.66 \mu \mathrm{~A}$
(4) $3.6 \mu \mathrm{~A}$
2. The threshold voltage of CMOS inverter $\left(\mathrm{V}_{\mathrm{th}}=\mathrm{V}_{\mathrm{in}}=\mathrm{V}_{\mathrm{out}}\right)$ can be expressed as
(1) $\frac{\mathrm{V}_{\text {ton }}+\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}\left(\mathrm{V}_{\mathrm{DD}}+\mathrm{V}_{\text {top }}\right)}{1+\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}}$
(2) $\frac{\mathrm{V}_{\text {ton }}-\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}\left(\mathrm{V}_{\mathrm{DD}}+\mathrm{V}_{\text {ton }}\right)}{1-\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}}$
(3) $\frac{\mathrm{V}_{\text {top }}+\sqrt{\frac{1}{k_{R}}}\left(\mathrm{~V}_{\mathrm{DD}}+\mathrm{V}_{\text {ton }}\right)}{1-\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}}$
(4) $\frac{\mathrm{V}_{\text {top }}-\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}\left(\mathrm{V}_{\mathrm{DD}}+\mathrm{V}_{\text {top }}\right)}{1+\sqrt{\frac{1}{\mathrm{k}_{\mathrm{R}}}}\left(\mathrm{V}_{\text {ton }}\right)}$
3. Consider a unity feedback control system as shown in the following figure :


The steady state error is given as
(1) $\lim _{\mathrm{s} \rightarrow 0} \frac{\mathrm{R}(\mathrm{S})}{1+\mathrm{SG}(\mathrm{S})}$
(2) $\lim _{\mathrm{S} \rightarrow 0} \frac{\mathrm{SR}(\mathrm{S})}{1+\mathrm{G}(\mathrm{S})}$
(3) $\lim _{s \rightarrow 0} \frac{\mathrm{SR}(\mathrm{S})}{1-\mathrm{G}(\mathrm{S})}$
(4) $\lim _{\mathrm{s} \rightarrow 0} \frac{\mathrm{R}(\mathrm{S})}{1-\mathrm{SG}(\mathrm{S})}$
4. A conducting line on an IC chip is 2.8 mm long and has a rectangular cross-section $1 \times 4$ micrometer. A current of 5 mA produces a voltage drop of 100 mV across the line. If the electron mobility is $500 \mathrm{~cm}^{2} /$ V.s., the electron concentration is
(1) $4.38 \times 10^{21} \mathrm{~cm}^{-3}$
(2) $4.38 \times 10^{27} \mathrm{~cm}^{-3}$
(3) $3.98 \times 10^{21} \mathrm{~cm}^{-3}$
(4) $4.29 \times 10^{27} \mathrm{~cm}^{-3}$
5. In emitter bias configuration, the stability factor $\mathrm{S}\left(\mathrm{I}_{\mathrm{CO}}\right)$ is defined as
(1) $\quad(\beta+1) \frac{1+\frac{R_{B}}{R_{E}}}{\frac{R_{B}}{R_{E}}+(\beta+1)}$
(2) $\frac{1+\frac{R_{B}}{R_{E}}}{\frac{R_{B}}{R_{E}}+(\beta+1)}$
(3) $\frac{(\beta+1)}{(\beta+1)+\frac{\mathrm{R}_{\mathrm{B}}}{\mathrm{R}_{\mathrm{E}}}}$
(4) $(\beta+1) \frac{1+\frac{\mathrm{R}_{\mathrm{B}}}{\mathrm{R}_{\mathrm{E}}}}{(\beta+1)+\frac{\mathrm{R}_{\mathrm{E}}}{\mathrm{R}_{\mathrm{B}}}}$
6. The following state diagram belongs to

(1) J K flip flop
(2) R S flip flop
(3) T flip flop
(4) D flip flop
7. 8086 is interfaced to two 8259s (programmable interval timer). If 8259s are in master slave configuration, the number of interrupts available to the 8086 microprocessor is
(1) 8
(2) 16
(3) 32
(4) 64
8. A function q that accepts a pointer to a character as argument and returns a pointer to an array of integer, can be declared as
(1) $\operatorname{int} * \mathrm{q}(\mathrm{char} *)[]$
(2) $\quad \operatorname{int}(* q(c h a r ~ *))[]$
(3) $\operatorname{int}(* q)($ char $*)[]$
(4) None of the above
9. TRAPATT is used as
(1) amplifier in radars
(2) local oscillator in radars
(3) switch in communication systems
(4) low frequency oscillator
10. Tunnel diode is used as
(1) High speed switch
(2) Clipper
(3) Low gain amplifier
(4) Low frequency oscillator
11. The fourier series of the following figure is

(1) $\frac{8}{\pi^{2}}\left[\sin (\pi \mathrm{t})+\frac{1}{9} \sin (3 \pi \mathrm{t})+\frac{1}{25} \sin (5 \pi \mathrm{t})+\ldots.\right]$
(2) $\frac{8}{\pi^{2}}\left[\sin (\pi \mathrm{t})-\frac{1}{9} \cos (3 \pi \mathrm{t})+\frac{1}{25} \sin (5 \pi \mathrm{t})+\ldots.\right]$
(3) $\frac{8}{\pi^{2}}\left[\cos (\pi \mathrm{t})+\frac{1}{9} \cos (3 \pi \mathrm{t})+\frac{1}{25} \cos (5 \pi \mathrm{t})+\ldots.\right]$
(4) $\frac{8}{\pi^{2}}\left[\cos (\pi \mathrm{t})-\frac{1}{9} \sin (3 \pi \mathrm{t})+\frac{1}{25} \cos (5 \pi \mathrm{t})+\ldots.\right]$
12. Which of the following cannot be the Fourier series expansion of a periodic signal ?
(1) $x(t)=2 \cos t+3 \cos 3 t$
(2) $x(t)=2 \cos \pi t+7 \cos t$
(3) $x(t)=\cos t+0.5$
(4) $x(t)=2 \cos 1.5 \pi t+\sin 3.5 \pi t$
13. The main advantage of TDM over FDM is that it
(1) needs less power
(2) needs less bandwidth
(3) needs simple circuitry
(4) gives better signal / noise ratio
TDM stands for Time Division Multiplexing
FDM stands for Frequency Division Multiplexing
14. Consider the RC circuit shown in the following figure


The differential equation of the system will be
(1) $\frac{\mathrm{de}_{\mathrm{i}}(\mathrm{t})}{\mathrm{dt}}=\frac{\mathrm{e}_{\mathrm{o}}(\mathrm{t})}{R C}+\frac{\mathrm{de}_{\mathrm{o}}(\mathrm{t})}{\mathrm{dt}}$
(2) $\frac{\mathrm{de}_{\mathrm{i}}(\mathrm{t})}{\mathrm{dt}}=\frac{1}{\mathrm{RC}} \frac{\mathrm{de}_{\mathrm{o}}(\mathrm{t})}{\mathrm{dt}}+\mathrm{e}_{\mathrm{o}}(\mathrm{t})$
(3) $\frac{1}{R C} \frac{\mathrm{de}_{\mathrm{i}}(\mathrm{t})}{\mathrm{dt}}=\frac{1}{\mathrm{RC}} \frac{\mathrm{de}_{\mathrm{o}}(\mathrm{t})}{\mathrm{dt}}-\mathrm{e}_{\mathrm{o}}(\mathrm{t})$
(4) $\frac{\mathrm{de}_{\mathrm{i}}(\mathrm{t})}{\mathrm{dt}}=\frac{\mathrm{e}_{\mathrm{o}}(\mathrm{t})}{\mathrm{RC}}-\frac{\mathrm{de}_{\mathrm{o}}(\mathrm{t})}{\mathrm{dt}}$
15. The state-space model of a system is given as
$\left[\begin{array}{l}\dot{x}_{1} \\ \dot{x}_{2}\end{array}\right]=\left[\begin{array}{ll}1 & 0 \\ 1 & 1\end{array}\right]\left[\begin{array}{l}x_{1} \\ x_{2}\end{array}\right]+\left[\begin{array}{l}1 \\ 1\end{array}\right] \mathrm{u}(\mathrm{t})$
where $\mathrm{u}(\mathrm{t})$ is a unit step input occurring at $\mathrm{t}=0$ and $x(0)=\left[\begin{array}{l}1 \\ 0\end{array}\right]$.
The time response of the system is
(1) $\left[\begin{array}{c}2 \mathrm{e}^{\mathrm{t}}+1 \\ 2 \mathrm{t} \mathrm{e}^{-t}\end{array}\right]$
(2) $\left[\begin{array}{c}2 e^{t}-1 \\ 2 t e^{t}\end{array}\right]$
(3) $\left[\begin{array}{c}2 e^{-t}+1 \\ 2 t e^{-t}\end{array}\right]$
(4) $\left[\begin{array}{c}\mathrm{e}^{-\mathrm{t}}+1 \\ 2 \mathrm{e}^{\mathrm{t}}\end{array}\right]$
16. If ' $L$ ' is the length, ' $W$ ' is the width and ' $t$ ' is the thickness of a semiconductor layer to form a resistor, the sheet resistance is defined as
(a) $\frac{\rho}{\mathrm{t}}$
(b) $\frac{\rho L}{t \mathrm{~A}}$
(c) $\frac{\mathrm{R}}{\mathrm{t}}$
(d) $\frac{\mathrm{RW}}{\mathrm{L}}$

Out of the above which one is correct ?
(1)
(a) and (b) are correct.
(2) (b) and (d) are correct.
(3) (a) and (d) are correct.
(4) (b) and (c) are correct.
17. The typical dB magnitude versus characteristics of the compensators is given below :

(i)

(ii)

(iii)
(a) (i) is log compensator and (ii) is lead compensator
(b) (ii) is lead compensator and (i) is log compensator
(c) (ii) is lead compensator and (iii) is log compensator
(d) (i) is log compensator and (ii) is log-lead compensator

Which of the above are correct ?
(1)
(a) and (b)
(2) (b) and (c)
(3)
(a) and (d)
(4) (b) and (d)
18. In reverse bias abrupt p-n junction the depletion region thickness is
(a) $\sqrt{\frac{2 \epsilon_{\mathrm{si}}}{\mathrm{q}}\left(\frac{\mathrm{N}_{\mathrm{A}}+\mathrm{N}_{\mathrm{D}}}{\mathrm{N}_{\mathrm{A}} \mathrm{N}_{\mathrm{D}}}\right)\left(\phi_{\mathrm{o}}-\mathrm{V}\right)}$
(b) $\sqrt{\frac{2 \epsilon_{\text {si }}}{\mathrm{q}}\left(\frac{\mathrm{N}_{\mathrm{A}} \mathrm{N}_{\mathrm{D}}}{\mathrm{N}_{\mathrm{A}}+\mathrm{N}_{\mathrm{D}}}\right)\left(\phi_{\mathrm{o}}-\mathrm{V}\right)}$
(c) $\sqrt{\frac{2 \epsilon_{\mathrm{si}}}{\mathrm{q}}\left(\frac{\mathrm{N}_{\mathrm{A}}-\mathrm{N}_{\mathrm{D}}}{\mathrm{N}_{\mathrm{A}}+\mathrm{N}_{\mathrm{D}}}\right)\left(\mathrm{V}-\phi_{\mathrm{o}}\right)}$
(d) $\sqrt{\frac{2 \epsilon_{\text {si }}}{\mathrm{q}}\left(\frac{\mathrm{N}_{\mathrm{A}} \mathrm{N}_{\mathrm{D}}}{\mathrm{N}_{\mathrm{A}}-\mathrm{N}_{\mathrm{D}}}\right)} \mathrm{V}$

Out of these which is correct answer
(1) (a) is correct and (b) is wrong
(2) (b) is correct and (c) is wrong
(3)
(a) and (b) are correct
(4) (d) is correct and (a) is wrong
19. For the counter to count upwards starting from 0000 to 1111 , following statement are given :
(a) LOAD input is high
(b) LOAD input is high
(c) CLEAR is high
(d) Enable input is low

Out of the above, the following is the correct answer :
(1)
(a), (b) and (d)
(2) (b), (c) and (d)
(3) (a), (c) and (d)
(4) (b) and (c)
20. Assume that the 8255 gets selected whenever A15 - A11 are high during I/O read or write cycles. The A2 and A1 are connected to A1 and A0 of 8255 chip, then the address for port C of 8255 is
(a) FEh
(b) 03 h
(c) FF03h
(d) FFh

## Codes :

(1)
(a) and (b) are correct
(2) (c) and (d) are correct
(3)
(a) and (d) are correct
(4) (b) and (d) are correct
21. A function $x y z$ is defined as:

Void xyz (int $\mathrm{a}=0$, int b , int $\mathrm{c}=0$ )
\{

```
        cout << a << b << c;
```

\}
Which of the following calls are illegal ?
(Assume h, g are declared as integers)
(a) $x y z()$;
(b) $x y z(h, h)$;
(c) $\mathrm{xyz}(\mathrm{h})$;
(d) $x y z(g, ~ g)$;

## Codes :

(1)
(a) and (c) are correct
(2) (b) and (d) are correct
(3)
(a) and (b) are correct
(4) (b) and (c) are correct
22. For transmission lines, following statements are given :
(a) For open circuited transmission line VSWR is $\infty$.
(b) For short circuited transmission line VSWR is 0 .
(c) For short circuited transmission line VSWR is $\infty$.
(d) The cut off frequency for TEM wave is 0 Hz .

Out of the above statements, following is correct :
(1) (a), (b)
(2) (a), (c)
(3) (a), (b) and (d)
(4) (a), (c) and (d)
23. Which of the following statements are correct ?
(a) PAM can be detected by a bandpass filter.
(b) Flat-top sampling leads to an aperture effect.
(c) Noise can be reduced by increasing sampling rate.
(d) Pulse code modulation is form of digital modulation.

## Codes :

(1) (a), (b) and (d) are correct
(2) (a), (b) and (c) are correct
(3) (a) and (b) are correct
(4) (b) and (d) are correct
24. A SCR may be turned OFF by
(a) Interrupting its anode current.
(b) Reversing polarity of its anode cathode voltage.
(c) Low current drop out
(d) When the gate return to 0 V after the trigger pulse is removed.

## Codes :

(1) (a), (b) and (c) are correct
(2) (a), (b) and (d) are correct
(3) (b), (c) and (d) are correct
(4) (a) and (b) are correct
25. Consider the following statements regarding Fourier transform :
(a) The Fourier transform of an impulse function is a constant function.
(b) The Fourier transform of an impulse function is a sine function
(c) The Fourier transform of a constant function is an impulse function.
(d) The Fourier transform of a constant function is a signum function. Which of the above statements are correct ?
(1)
(a) and (b)
(2) (b) and (c)
(3) (b) and (d)
(4) (a) and (c)
26. Match the following lists :

List - I
List - II
a. MOSFET
i. $\quad \mathrm{I}_{\mathrm{dss}}\left(1-\frac{\mathrm{V}_{\mathrm{gs}}}{\mathrm{V}_{\mathrm{p}}}\right)^{2}$
b. JFET
c. PN Junction diode
d. BJT
ii. $\quad \mathrm{K} \frac{\mathrm{W}}{\mathrm{L}}\left(\mathrm{V}_{\mathrm{gs}}-\mathrm{V}_{\mathrm{th}}\right) \mathrm{V}_{\mathrm{ds}}$
iii. $I_{o}\left(e \frac{q V}{k T}-1\right)$
iv. $\beta=\frac{I_{C}}{I_{B}}$

Codes:

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | i | iii | iv |
| (2) | i | iii | ii | iv |
| (3) | iii | ii | iv | i |
| (4) | i | ii | iv | iii |

27. Match the following lists :

List - I
a. Electro-encephalogram
b. Electro-myogram
c. Electrogastogram
d. Electro-oculogram

Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | iii | iv | i |
| (2) | iii | iv | i | ii |
| (3) | iv | i | ii | iii |
| (4) | i | iii | iv | ii |

28. Match the following lists :

List - I
a. Si-band gap
b. Ge-band gap
c. Ga As band gap
d. Si C band gap

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | iii | iv | i |
| (2) | i | ii | iv | iii |
| (3) | ii | i | iii | iv |
| (4) | iv | ii | iii | i |

## List - II

i. $\quad 3.3 \mathrm{eV}$
ii $\quad 1.12 \mathrm{eV}$
iii. 0.7 eV
iv. 1.43 eV

List - II
i. Electrical activity of brain
ii. Eye movements
iii. Electrical activity of skeletal muscles
iv. Muscle activity in the gastro intestinal
29. Match the following lists :

List - I
List - II
a. IC 74198 i. Quad D flip flop
b. IC 74175 ii J K master slave flip flop
c. IC 74104 iii. 8 bit shift register
d. IC 74132 iv. Quad Schmitt trigger

Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | i | ii | iii | iv |
| $(2)$ | ii | i | iii | iv |
| $(3)$ | iv | ii | i | iii |
| $(4)$ | iii | i | ii | iv |

30. Match the following in context of 8086 :

List - I
a. MOV AL, [5923 h]
b. MOV AL, [BX]
c. MOV DL, OAh [BX]
d. MOV AL, F5h [BP] [SI]

List - II
i. Register relative addressing
ii. Memory operand addressing mode
iii. Relative base index addressing
iv. Register indirect addressing

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | i | iv | iii |
| (2) | i | ii | iv | iii |
| (3) | iii | i | ii | iv |
| (4) | ii | iv | i | iii |

31. Match the following lists :

List - I
a. Array
b. Union
c. Volatile
d. Bit-field

## List - II

i. Heterogeneous
ii. Homogeneous
iii. Structure
iv. Qualifier

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | iii | i | iv |
| (2) | ii | i | iv | iii |
| (3) | i | iii | ii | iv |
| (4) | i | ii | iii | iv |

32. Match the following lists :

> List - I

List - II
a. VSWR
b. Reflection coefficient
c. Propagation coefficient
d. Characteristic impedance
i. $\leq 1$
ii. $\sqrt{\frac{(R+j \omega L)}{(G+j \omega C)}}$
iii. $\sqrt{(R+j \omega L)(G+j \omega C)}$
iv. $\geq 1$

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | i | iv | ii | iii |
| (2) | iv | i | iii | ii |
| (3) | iii | i | ii | iv |
| (4) | i | iii | iv | ii |

33. Match the following lists :

|  | List - I <br> (Frequency Range) |  | List - II <br> (Typical Application) |
| :--- | :--- | :--- | :--- |
| a. | $300 \mathrm{kHz}-3 \mathrm{MHz}$ | i. | FM Broadcasting, TV |
| b. | $430 \mathrm{THz}-750 \mathrm{THz}$ | ii. | Satellite communication |
| c. | $30 \mathrm{GHz}-300 \mathrm{GHz}$ | iii. | AM Broadcasting |
| d. | $30 \mathrm{MHz}-300 \mathrm{MHz}$ | iv. | Optical communication |

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | iii | iv | i | ii |
| (2) | iii | iv | ii | i |
| (3) | iv | iii | i | ii |
| (4) | iv | iii | ii | i |

34. Match the following lists :

## List - I

## List - II

a. Photodiode i. Prevent high voltages from affecting the system receiving the signal.
b. LASER
ii. Power consumption is negative
c. Solar cell
iii. Exhibits increase in reverse current with light intensity
d. Opto-coupler iv. Degenerate semi-conductors

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | iv | iii | i | ii |
| (2) | i | iv | ii | iii |
| (3) | i | iv | iii | ii |
| (4) | iii | iv | ii | i |

35. Match the following lists :
List - I
i.

b.

ii

c.

iii.

d.


iv.

## Codes :

|  | a | b | c | d |
| :---: | :---: | :---: | :---: | :---: |
| (1) | ii | i | iv | iii |
| (2) | i | iv | iii | ii |
| (3) | ii | i | iii | iv |
| (4) | i | iii | iv | ii |

## Directions : Question No. 36 to 45 :

The following items consist of two statements, one labelled as "Assertion (A)" and the other labelled as the "Reason (R)". You are to examine the two statements carefully and decide if the Assertion (A) and the Reason (R) are individually true and if so whether the reason is a correct explanation of the assertion. Select your answer to these items using the codes given below and mark your answer accordingly.

## Codes :

(1) Both (A) and (R) are true and (R) is the correct explanation of (A).
(2) Both (A) and (R) are true, but (R) is not the correct explanation of (A).
(3) (A) is true, but (R) is false.
(4) (A) is false, but ( $R$ ) is true.
36. Assertion (A) : MOSFET can be used to drive high current and high voltage networks without drawing current or power from the driving circuits.
Reason (R) : The high input impedances of FET essentially isolates the two parts of the network without the need of optical or electromagnetic linkages.
37. Assertion (A) : Duality is a property of circuit equations.

Reason (R) : Two circuits are said to be dual when the mesh equations that describe the behaviour of one circuit are found to be identical in the form of nodal equations that describe the other.
38. Assertion (A) : Clippers are networks that employ diodes to clip away a portion of an input signal without distorting the remaining part of the applied waveforms.

Reason (R) : In clipper circuit, the orientation of diode is very important and it decide the portion of the wave to be clipped. Clippers may be series or parallel type, depending upon the series/parallel connection of diode with the load.
39. Assertion (A) : In a conservative field the work done in moving from one point to another is independent of the path.

Reason (R) : In a conservative field, there is a mechanism for dissipating energy corresponding to positive work done and a source from which energy could be absorbed, if the work were negative.
40. Assertion (A) : For DAA instruction in $8086 \mu \mathrm{P}$, if four LSBs of register AL are greater than 9 or $\mathrm{AC}=1$ then 6 is added to the four LSBs of register AL.

Reason (R) : Because AL contains binary sum which needs to be converted into BCD sum.
41. Assertion (A) : In ' $C$ ' language, if ' $a$ ' is name of an array and '++' operator is used to increment the value of ' $a$ ' then compiler will flag an error.

Reason (R) : 'a' is 'const' type pointer.
42. Assertion (A) : Noise margin may be defined as the allowable level of additive noise for proper implementation of logic value at 0 and 1 without any possibility of misinterpretation.

Reason (R) : Noise Margin (NM), It may be defined as for high level : $\mathrm{NM}_{\mathrm{H}}=\mathrm{V}_{1 \mathrm{H}, \min }-\mathrm{V}_{\mathrm{OH}, \min }$ Further for low level, it is defined as : $\mathrm{NM}_{\mathrm{L}}=\mathrm{V}_{\mathrm{IL}, \max }-\mathrm{V}_{\mathrm{OL}, \max }$
43. Assertion (A) : Baseband designates the band of frequencies representing the signal supplied by the source of information.

Reason (R) : The frequency of baseband signal is equal to the carrier wave signal frequency.
44. Assertion (A) : In Lasers, the photons emitted has the same energy as the incident photon and are in phase with it.

Reason (R) : The amplitudes of individual photons add to produce a brighter light.
45. Assertion (A) : If the characteristic equation contains any root on the imaginary axis, then the output response will have sustained oscillations.

Reason (R) : The poles or the roots of the characteristic equation should not lie in the right half of s-plane for system stability.

Read the passage and answer the following questions numbering from 46 - 50 :

The physics of semiconductor devices is dependent on the physics of semiconductors itself. The most important semiconductors are Germanium, Silicon and Gallium Arsenide. The GaAs has different properties than Silicon or Germanium. Pure silicon is intrinsic and contains negligibly small amount of impurities. Each silicon atom shares its four valance electrons with the four neighbouring atoms forming covalent bonds. An extrinsic silicon is achieved by introducing the impurities. The band structure of a solid can be obtained by solving a Schrodinger equation of an approximate one electron problem.
46. The law of mass action in semiconductor is
(1) $n+p=n_{i}^{2}$
(2) $n \cdot p=n_{i}^{2}$
(3) $n \cdot p=n_{i}$
(4) $n^{2} p=n_{i}^{2}$
47. The Fermi-Dirac distribution function is
(1) $\frac{1}{1-\exp \left(\frac{\mathrm{E}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}$
(2) $\frac{1}{1+\exp \left(\frac{\mathrm{E}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}$
(3) $\frac{\Delta \mathrm{E}}{1+\exp \left(\frac{\mathrm{E}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}$
(4) $\frac{\Delta \mathrm{E}}{1-\exp \left(\frac{\mathrm{E}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}$
48. The Fermi level for the extrinsic semiconductor (n-type) lies
(1) close to the middle of the bandgap
(2) below the conduction band
(3) above the conduction band
(4) above the valance band
49. The number of ionised donors are given by
(1) $\mathrm{N}_{\mathrm{D}}^{+}=\mathrm{N}_{\mathrm{D}}\left[1-\frac{1}{1+\frac{1}{g} \exp \left(\frac{E_{\mathrm{D}}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}\right]$
(2) $\mathrm{N}_{\mathrm{D}}^{+}=\mathrm{N}_{\mathrm{D}}\left[1+\frac{1}{1+\frac{1}{g} \exp \left(\frac{\mathrm{E}_{\mathrm{D}}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)}\right]$
(3) $\mathrm{N}_{\mathrm{D}}^{+}=\mathrm{N}_{\mathrm{D}}\left[1+\frac{1}{g} \exp \left(\frac{\mathrm{E}_{\mathrm{D}}-\mathrm{E}_{\mathrm{F}}}{\mathrm{kT}}\right)\right]$
(4) $\mathrm{N}_{\mathrm{D}}=\mathrm{N}_{\mathrm{D}}^{+}\left[1+\frac{1}{\mathrm{~g}} \exp \left(\frac{\mathrm{E}_{\mathrm{F}}-\mathrm{E}_{\mathrm{D}}}{\mathrm{kT}}\right)\right]$
50. In intrinsic semi-conductor the number of occupied conduction band levels are
(1) $\int_{0}^{E_{C}} N(E) F(E) d E$
(2) $\int_{E_{C}}^{E_{\text {top }}} N(E) F(E) d E$
(3) $\int_{E_{v}}^{E_{C}} N(E) F(E) d E$
(4) $\int_{E_{C}}^{E_{V}} N(E) F(E) d E$

## Space For Rough Work

